

L Number	Hits	Search Text	DB	Time stamp
1	174	(711/101).CCLS.	USPAT	2004/04/23 08:50
2	719	(711/103).CCLS.	USPAT	2004/04/23 08:50
3	1052	(711/118).CCLS.	USPAT	2004/04/23 08:50
4	308	(711/119).CCLS.	USPAT	2004/04/23 08:50
5	174	(711/120).CCLS.	USPAT	2004/04/23 08:50
6	162	(711/123).CCLS.	USPAT	2004/04/23 08:50
7	83	(711/126).CCLS.	USPAT	2004/04/23 08:50
8	849	(711/167).CCLS.	USPAT	2004/04/23 08:50
9	492	(711/3).CCLS.	USPAT	2004/04/23 08:50
10	324	((cache\$1 near2 (hit\$4 or resid\$3 or contain\$3)) same ((next or single or one) with (clock or period or cycle))) and ((cache\$1 near2 (mishit\$4 or misshit\$4 or miss\$3)) same ((delay\$3 or wait\$3) or ((plural\$3 or multip\$6 or several or many) near2 (clocks or periods or cycles))))	USPAT	2004/04/23 09:09
11	93	((711/101).CCLS.) or ((711/103).CCLS.) or ((711/118).CCLS.) or ((711/119).CCLS.) or ((711/120).CCLS.) or ((711/123).CCLS.) or ((711/126).CCLS.) or ((711/167).CCLS.) or ((711/3).CCLS.) and (((cache\$1 near2 (hit\$4 or resid\$3 or contain\$3)) same ((next or single or one) with (clock or period or cycle))) and ((cache\$1 near2 (mishit\$4 or misshit\$4 or miss\$3)) same ((delay\$3 or wait\$3) or ((plural\$3 or multip\$6 or several or many) near2 (clocks or periods or cycles))))	USPAT	2004/04/23 09:11
12	9034	cache with (send\$3 or signal\$4 or transmit\$4 or control\$4 or inform\$3) with (processor\$1 or cpu\$1 or microprocessor\$1 or user\$1 or host\$1)	USPAT	2004/04/23 09:07
13	104	((cache\$1 near2 (hit\$4 or resid\$3 or contain\$3)) same ((next or single or one) with (clock or period or cycle))) same (cache with (send\$3 or signal\$4 or transmit\$4 or control\$4 or inform\$3) with (processor\$1 or cpu\$1 or microprocessor\$1 or user\$1 or host\$1))	USPAT	2004/04/23 09:11
14	172	((cache\$1 near2 (mishit\$4 or misshit\$4 or miss\$3)) same ((delay\$3 or wait\$3) or ((plural\$3 or multip\$6 or several or many) near2 (clocks or periods or cycles)))) same (cache with (send\$3 or signal\$4 or transmit\$4 or control\$4 or inform\$3) with (processor\$1 or cpu\$1 or microprocessor\$1 or user\$1 or host\$1))	USPAT	2004/04/23 09:11
15	81	((711/101).CCLS.) or ((711/103).CCLS.) or ((711/118).CCLS.) or ((711/119).CCLS.) or ((711/120).CCLS.) or ((711/123).CCLS.) or ((711/126).CCLS.) or ((711/167).CCLS.) or ((711/3).CCLS.) and (((cache\$1 near2 (hit\$4 or resid\$3 or contain\$3)) same ((next or single or one) with (clock or period or cycle))) same (cache with (send\$3 or signal\$4 or transmit\$4 or control\$4 or inform\$3) with (processor\$1 or cpu\$1 or microprocessor\$1 or user\$1 or host\$1))) or (((cache\$1 near2 (mishit\$4 or misshit\$4 or miss\$3)) same ((delay\$3 or wait\$3) or ((plural\$3 or multip\$6 or several or many) near2 (clocks or periods or cycles)))) same (cache with (send\$3 or signal\$4 or transmit\$4 or control\$4 or inform\$3) with (processor\$1 or cpu\$1 or microprocessor\$1 or user\$1 or host\$1))))	USPAT	2004/04/23 09:11

16	38	((cache\$1 near2 (mishit\$4 or misshit\$4 or miss\$3)) same ((delay\$3 or wait\$3) or ((plural\$3 or multip\$6 or several or many) near2 (clocks or periods or cycles)))) same (cache with (send\$3 or signal\$4 or transmit\$4 or control\$4 or inform\$3) with (processor\$1 or cpu\$1 or microprocessor\$1 or user\$1 or host\$1))	US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/23 09:12
17	25	((cache\$1 near2 (hit\$4 or resid\$3 or contain\$3)) same ((next or single or one) with (clock or period or cycle))) same (cache with (send\$3 or signal\$4 or transmit\$4 or control\$4 or inform\$3) with (processor\$1 or cpu\$1 or microprocessor\$1 or user\$1 or host\$1))	US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/23 09:12